



EXPEDITED PROCEDURE – EXAMINING GROUP 2841

S/N 10/646,478

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Daisuke Kawagoe	Examiner:	Ishwar B. Patel
Serial No.:	10/646,478	Group Art Unit:	2841
Filed:	August 22, 2003	Docket No.:	884.937US1
Title:	STACKED VIA STRUCTURE THAT INCLUDES A SKIP VIA		
Assignee:	Intel Corporation	Customer Number:	21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed June 29, 2005, please amend the application as follows: